**Listing of Claims**

This listing of claims will replace all prior versions, and listings, of claims in the application.

1    **1.**    (Currently Amended) A voice data processing system, comprising:

2    at least one status store for storing status information on a plurality of

3    voice channel;

4    at least one source buffer for storing voice data for the plurality of

5    voice channels <u>and including a valid store for storing at least a valid indication</u>

6    <u>or invalid indication for each voice channels, the valid indication indicating</u>

7    <u>that the corresponding voice channel data is valid, the invalid indication</u>

8    <u>indicating that the corresponding voice channel is not valid</u>; and

9    a processing system that accesses the at least one status store and

10    generates read addresses for the at least one source buffer to access voice data

11    for only those voice channels having a predetermined status, and generates

12    write addresses for at least one destination buffer for writing the voice data

13    read from the at least one source buffer to at least one destination buffer<u>, and</u>

14    <u>wherein the processing system reads voice data from the at least source buffer</u>

15    <u>and writes the voice data to the at least one destination buffer for only those</u>

16    <u>voice channels having a valid indication.</u>

1    **2.**    Cancelled.

1    **3.**    (currently amended) A voice data processing system, comprising ~~The voice data~~

2    ~~processing system of claim 1, wherein~~:

3    <u>at least one status store for storing status information on a plurality of</u>

4    <u>voice channels,</u> the at least one status store ~~includes~~ <u>including</u> a silent store

5    that stores at least a silent indication or an active indication for each voice

6    channel, the silent indication indicating the corresponding voice channel is not

7    carrying voice data, the active indication indicating that the corresponding

8    voice channel is carrying voice data;

9    at least one source buffer for storing voice data for the plurality of

10   voice channels and including a local buffer coupled to a synchronous data

11   network; and

12   a processing system that accesses the at least one status store and

13   generates read addresses for the at least one source buffer to access voice data

14   for only those voice channels having a predetermined status, and generates

15   write addresses for at least one destination buffer for writing the voice data

16   read from the at least one source buffer to at least one destination buffer, the

17   processing system includes-including an egress engine, the at least one source

18   buffer includes a local buffer coupled to a synchronous data network, and the

19   at least one destination buffer includes including a voice packet buffer

20   memory (VPBM) coupled to an asynchronous data network, the egress engine

21   outputting voice data and VPBM addresses for those voice channels when the

22   corresponding silent store indication is active, and does not output voice data

23   and the VPBM address when the corresponding silent store indication is

24   silent.

1   **4.**    (original) The voice data processing system of claim 3, wherein:

2    the processing system includes an address generator that generates

3    write addresses from at least a time stamp count value.

1   **5.**    (original) The voice data processing system of claim 4, wherein:

2         the VPBM stores voice channel information according to a voice data

3         sample size value, and the write address is generated from more bits of a

4         timestamp counter for larger sample size values.


1  **6.**    (original) The voice data processing system of claim 1, wherein:

2         the status store comprises a dual port memory.


1  **7.**    (original) The voice data processing system of claim 1, wherein:

2         the processing system includes a first-in-first-out buffer (FIFO) that

3    stores voice data and corresponding at least one destination buffer addresses

4    for those voice channels having the predetermined status.


1  **8.**    Cancelled.


1  **9.**    (currently amended) A <u>voice data processing system, comprising</u> ~~The voice data~~

2        ~~processing system of claim 8, wherein~~:

3         <u>at least one status store for storing status information on a plurality of voice</u>

4    <u>channel;</u>

5         <u>at least one source buffer for storing voice data for the plurality of</u>

6    <u>voice channels; and</u>

7         <u>a processing system that accesses the at least one status store and</u>

8    <u>generates read addresses for the at least one source buffer to access voice data</u>

9    <u>for only those voice channels having a predetermined status, generates write</u>

10    <u>addresses for at least one destination buffer for writing the voice data read</u>

11    <u>from the at least one source buffer to at least one destination buffer, and</u>

12    <u>includes an ingress engine, the at least one source buffer includes a jitter</u>

13    <u>buffer coupled to an asynchronous data network, and the at least one</u>

14    <u>destination buffer includes a local buffer coupled to a synchronous data</u>

15        network,. and further the processing system includes an address generator that

16        generates write addresses for the local buffer from a status store address.

1   **10.**   (currently amended) A voice data processing system, comprising The voice data

2        processing system of claim 8, wherein:

3            at least one status store for storing status information on a plurality of voice

4        channel;

5            at least one source buffer for storing voice data for the plurality of

6        voice channels; and

7            a processing system that accesses the at least one status store and

8        generates read addresses for the at least one source buffer to access voice data

9        for only those voice channels having a predetermined status, generates write

10        addresses for at least one destination buffer for writing the voice data read

11        from the at least one source buffer to at least one destination buffer, includes

12        an ingress engine, the at least one source buffer includes a jitter buffer coupled

13        to an asynchronous data network, and the at least one destination buffer

14        includes a local buffer coupled to a synchronous data network,. and further the

15        processing system includes an address generator that generates read addresses

16        for the jitter buffer from at least a time stamp value.

1   **11.**   (original) The voice data processing system of claim 10, wherein:

2            the jitter buffer stores voice channel information according to a voice

3        data sample size value; and

4            the address generator generates read addresses from more bits of a

5        timestamp counter for larger sample size values.

1   **12.**   (original) The voice data processing system of claim 10, wherein:

2            the address generator generates read addresses for the jitter buffer from

3        at least a portion of read addresses for the status store.

1   **13.**   (original) The voice data processing system of claim 12, wherein:

2          the address generator further generates read addresses for the jitter

3   buffer by adding a base address to an offset address generated from at least a

4   portion of read addresses for the status store and a time stamp count value.

1   **14.**   (original) The voice data processing system of claim 1, further including:

2          a data packing circuit that accesses at least one compression status

3   store and packs compressed data into the at least one destination buffer for

4   only those voice channels having a predetermined compression status.

1   **15.**   (original) The voice data processing system of claim 1, further including:

2          a data unpacking circuit that accesses at least one uncompression

3   status store and unpacks uncompressed data into the at least one destination

4   buffer for only those voice channels having a predetermined uncompression

5   status.

1   **16.**   (original) A voice data processing system, comprising:

2          at least one local buffer for storing voice data for a plurality of

3   channels that is received from a synchronous voice data source;

4          at least one voice packet buffer memory (VPBM) for storing voice

5   data for the plurality of channels for transmission over an asynchronous data

6   network;

7          at least one status store for storing status information for the plurality

8   of voice channels; and

9          an address generator that accesses the at least one status store for the

10   status information and generates local buffer read addresses and VPBM write

11   addresses for only those voice channels having a first type of status

12   information, the VPBM write addresses being generated from at least one

13   timestamp value.

1    17.    (original) The voice data processing system of claim 16, wherein:

2            the at least one voice data store includes a first buffer and second

3    buffer, the first local buffer storing voice channel information for a different

4    time period than the second local buffer.


1    18.    (original) The voice data processing system of claim 16, wherein:

2            the VPBM comprises a plurality of channel entry groups, each channel

3    entry group including $2^x$ channels, where x is an integer; and

4            the address generator adds the timestamp value to x bits of the base

5    address value to generate the VPBM write address.


1    19.    (original) The voice data processing system of claim 16, wherein:

2            the VPBM comprises a plurality of channel entry groups arranged

3    according to a buffer size value, each channel entry group being addressable

4    by a base address, the address generator increments a first base address by a

5    voice channel number value multiplied by a buffer size value to generate base

6    addresses.

1    20.    (original) A voice data processing system, comprising:

2            at least one local buffer for storing voice data for a plurality of

3    channels for transmission over a synchronous voice data source;

4            at least one jitter buffer memory for storing voice data for the plurality

5    of channels that is received from an asynchronous data network;

6            at least one status store for storing status information for the plurality

7    of voice channels; and

7

8        an address generator that accesses the at least one status store for the

9        status information and generates jitter buffer read addresses and local buffer

10        write addresses for only those voice channels having a first type of status

11        information, the jitter buffer write addresses being generated from at least one

12        timestamp value.


1   **21.**   (original) The voice data processing system of claim 20, wherein:

2        the at least one status store includes a plurality of addressable entries,

3        each entry including status information for a plurality of voice channels; and

4        the address generator combines priority decoded status store entry

5        values with a status store address for the status store entry to generate the local

6        buffer write addresses.


1   **22.**   (original) The voice data processing system of claim 21, wherein:

2        the at least one jitter buffer includes a first jitter buffer having a first

3        size and a second jitter buffer having a second size; and

4        the address generator combines group register value with

5        corresponding timestamp values to generate at least a first portion of the jitter

6        buffer read addresses, the timestamp values being masked according to the

7        size of the jitter buffer being read.